

# 7MBR25VA120-50

**IGBT Modules**

## IGBT MODULE (V series)

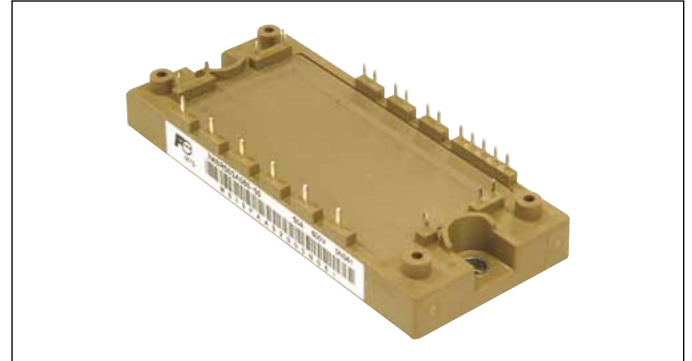
### 1200V / 25A / PIM

#### ■ Features

- Low  $V_{CE(sat)}$
- Compact Package
- P.C.Board Mount Module
- Converter Diode Bridge Dynamic Brake Circuit
- RoHS compliant product

#### ■ Applications

- Inverter for Motor Drive
- AC and DC Servo Drive Amplifier
- Uninterruptible Power Supply



#### ■ Maximum Ratings and Characteristics

##### ● Absolute Maximum Ratings (at $T_c=25^\circ\text{C}$ unless otherwise specified)

Items		Symbols	Conditions	Maximum ratings	Units	
Inverter	Collector-Emitter voltage	$V_{CES}$		1200	V	
	Gate-Emitter voltage	$V_{GES}$		$\pm 20$	V	
	Collector current	$I_c$	Continuous	$T_c=100^\circ\text{C}$	25	A
		$I_{cp}$	1ms	$T_c=80^\circ\text{C}$	50	
		$-I_c$			25	
		$-I_{c\ pulse}$	1ms		50	
Collector power dissipation	$P_c$	1 device		170	W	
Brake	Collector-Emitter voltage	$V_{CES}$		1200	V	
	Gate-Emitter voltage	$V_{GES}$		$\pm 20$	V	
	Collector current	$I_c$	Continuous	$T_c=80^\circ\text{C}$	25	A
		$I_{cp}$	1ms	$T_c=80^\circ\text{C}$	50	
	Collector power dissipation	$P_c$	1 device		170	W
	Repetitive peak reverse voltage (Diode)	$V_{RRM}$			1200	V
Converter	Repetitive peak reverse voltage	$V_{RRM}$		1600	V	
	Average output current	$I_o$	50Hz/60Hz, sine wave	25	A	
	Surge current (Non-Repetitive)	$I_{FSM}$	10ms, $T_j=150^\circ\text{C}$	155	A	
	$I^2t$ (Non-Repetitive)	$I^2t$	half sine wave	120	$\text{A}^2\text{s}$	
Junction temperature	$T_j$	Inverter, Brake		175	$^\circ\text{C}$	
		Converter		150		
Operating junction temperature (under switching conditions)	$T_{jop}$	Inverter, Brake		150		
		Converter		150		
Case temperature	$T_c$			125		
Storage temperature	$T_{stg}$			-40~+125		
Isolation voltage	between terminal and copper base (*1) between thermistor and others (*2)	$V_{iso}$	AC : 1min.	2500	VAC	
Screw torque	Mounting (*3)	-	M5	3.5	N m	

Note \*1: All terminals should be connected together during the test.

Note \*2: Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

Note \*3: Recommendable value : 2.5-3.5 Nm (M5)

● Electrical characteristics (at  $T_j = 25^\circ\text{C}$  unless otherwise specified)

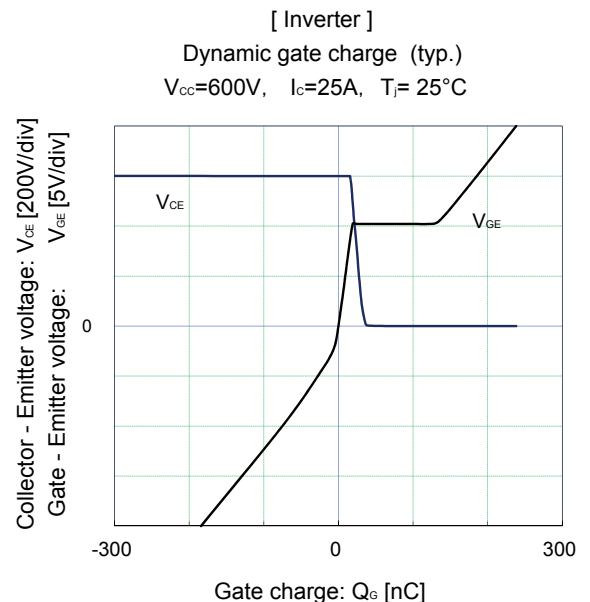
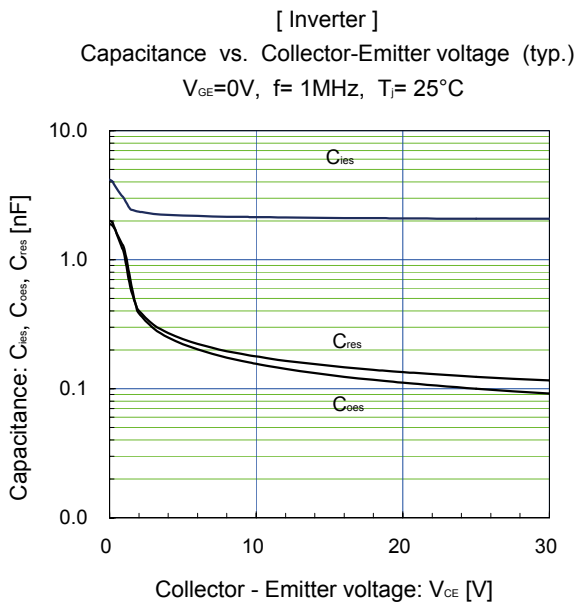
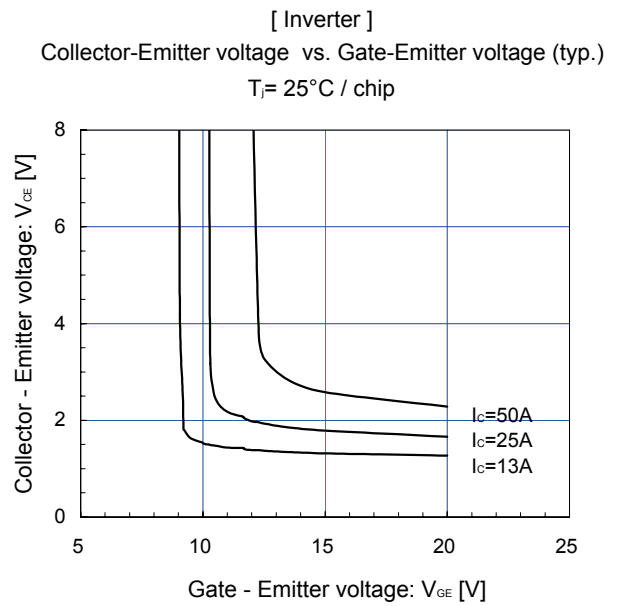
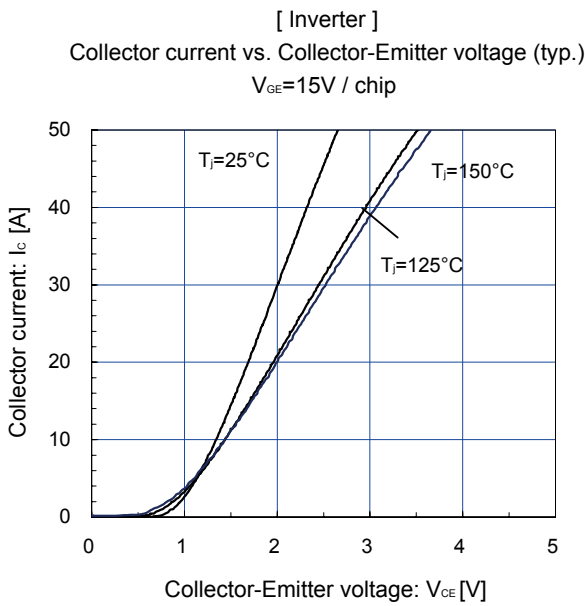
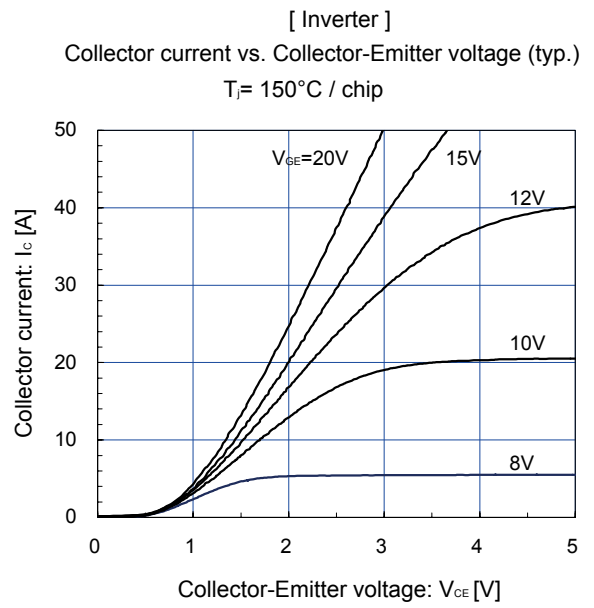
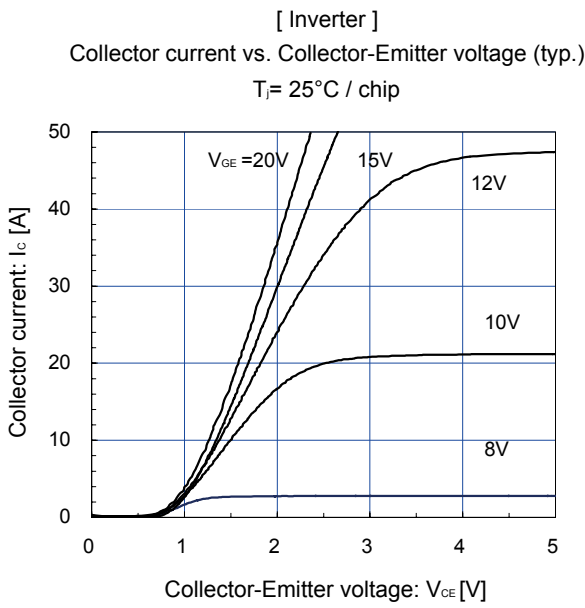
Items	Symbols	Conditions	Characteristics			Units		
			min.	typ.	max.			
Inverter	Zero gate voltage collector current	$I_{CES}$	$V_{GE} = 0V, V_{CE} = 1200V$	-	-	1.0	mA	
	Gate-Emitter leakage current	$I_{GES}$	$V_{GE} = 0V, V_{CE} = \pm 20V$	-	-	200	nA	
	Gate-Emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = 20V, I_c = 25mA$	6.0	6.5	7.0	V	
	Collector-Emitter saturation voltage	$V_{CE(sat)}$ (terminal)	$V_{GE} = 15V$ $I_c = 25A$	$T_j = 25^\circ\text{C}$	-	2.25	2.70	V
				$T_j = 125^\circ\text{C}$	-	2.60	-	
				$T_j = 150^\circ\text{C}$	-	2.65	-	
		$V_{CE(sat)}$ (chip)	$V_{GE} = 15V$ $I_c = 25A$	$T_j = 25^\circ\text{C}$	-	1.85	2.30	
				$T_j = 125^\circ\text{C}$	-	2.20	-	
	$T_j = 150^\circ\text{C}$	-	2.25	-				
	Internal gate resistance	$R_{g(int)}$	-	-	0	-	$\Omega$	
	Input capacitance	$C_{ies}$	$V_{CE} = 10V, V_{GE} = 0V, f = 1MHz$	-	2.1	-	nF	
	Turn-on time	$t_{on}$	$V_{CC} = 600V$ $I_c = 25A$ $V_{GE} = +15 / -15V$ $R_G = 39\Omega$	-	0.39	1.20	$\mu s$	
		$t_r$		-	0.09	0.60		
		$t_{r(i)}$		-	0.03	-		
	Turn-off time	$t_{off}$	$R_G = 39\Omega$	-	0.53	1.00	$\mu s$	
$t_t$		-		0.06	0.30			
Forward on voltage	$V_F$ (terminal)	$I_F = 25A$	$T_j = 25^\circ\text{C}$	-	2.10	2.55	V	
			$T_j = 125^\circ\text{C}$	-	2.25	-		
			$T_j = 150^\circ\text{C}$	-	2.20	-		
	$V_F$ (chip)	$I_F = 25A$	$T_j = 25^\circ\text{C}$	-	1.70	2.15		
			$T_j = 125^\circ\text{C}$	-	1.85	-		
$T_j = 150^\circ\text{C}$	-	1.80	-					
Reverse recovery time	$t_{rr}$	$I_F = 25A$	-	-	0.35	$\mu s$		
Brake	Zero gate voltage collector current	$I_{CES}$	$V_{GE} = 0V$ $V_{CE} = 1200V$	-	-	1.0	mA	
	Gate-Emitter leakage current	$I_{GES}$	$V_{CE} = 0V$ $V_{GE} = +20 / -20V$	-	-	200	nA	
	Collector-Emitter saturation voltage	$V_{CE(sat)}$ (terminal)	$V_{GE} = 15V$ $I_c = 25A$	$T_j = 25^\circ\text{C}$	-	2.25	2.70	V
				$T_j = 125^\circ\text{C}$	-	2.60	-	
				$T_j = 150^\circ\text{C}$	-	2.65	-	
		$V_{CE(sat)}$ (chip)	$V_{GE} = 15V$ $I_c = 25A$	$T_j = 25^\circ\text{C}$	-	1.85	2.30	
				$T_j = 125^\circ\text{C}$	-	2.20	-	
	$T_j = 150^\circ\text{C}$	-	2.25	-				
	Internal gate resistance	$R_{g(int)}$	-	-	0	-	$\Omega$	
	Turn-on time	$t_{on}$	$V_{CE} = 600V$ $I_c = 25A$ $V_{GE} = +15 / -15V$ $R_G = 39\Omega$	-	0.39	1.20	$\mu s$	
		$t_r$		-	0.09	0.60		
	Turn-off time	$t_{off}$	$R_G = 39\Omega$	-	0.53	1.00	$\mu s$	
		$t_t$		-	0.06	0.30		
	Reverse current	$I_{RRM}$	$V_R = 1200V$	-	-	1.00	mA	
	Converter	Forward on voltage	$I_F = 25A$	terminal	-	1.80	2.25	V
chip				-	1.42	-		
Reverse current	$I_{RRM}$	$V_R = 1600V$	-	-	1.0	mA		
Thermistor	Resistance	$T = 25^\circ\text{C}$	-	5000	-	$\Omega$		
		$T = 100^\circ\text{C}$	465	495	520			
B value	B	$T = 25 / 50^\circ\text{C}$	3305	3375	3450	K		

## ● Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
			min.	typ.	max.	
Thermal resistance (1device)	$R_{th(j-c)}$	Inverter IGBT	-	-	0.89	$^\circ\text{C/W}$
		Inverter FWD	-	-	1.06	
		Brake IGBT	-	-	0.89	
		Converter Diode	-	-	0.97	
Contact thermal resistance (1device) (*4)	$R_{th(c-f)}$	with Thermal Compound	-	0.05	-	

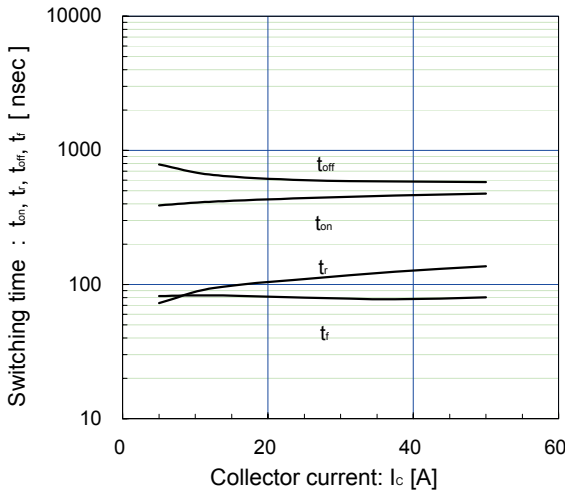
Note \*4: This is the value which is defined mounting on the additional cooling fin with thermal compound.

■ Characteristics (Representative)



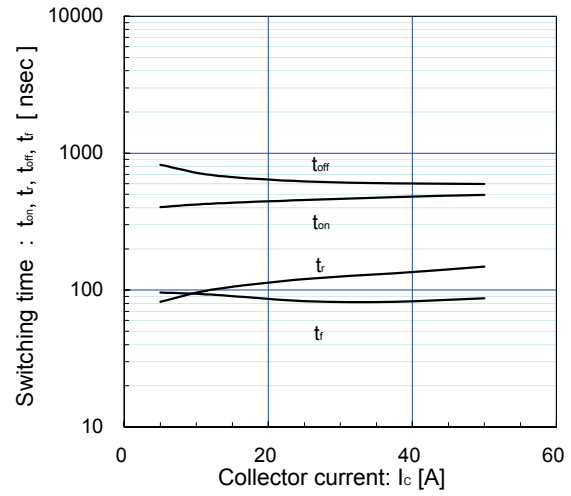
[ Inverter ]

Switching time vs. Collector current (typ.)  
 $V_{CC}=600V, V_{GE}=\pm 15V, R_G=39\Omega, T_J=125^\circ C$



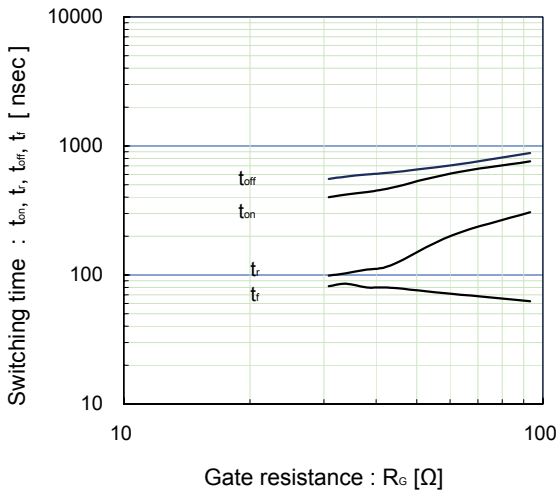
[ Inverter ]

Switching time vs. Collector current (typ.)  
 $V_{CC}=600V, V_{GE}=\pm 15V, R_G=39\Omega, T_J=150^\circ C$



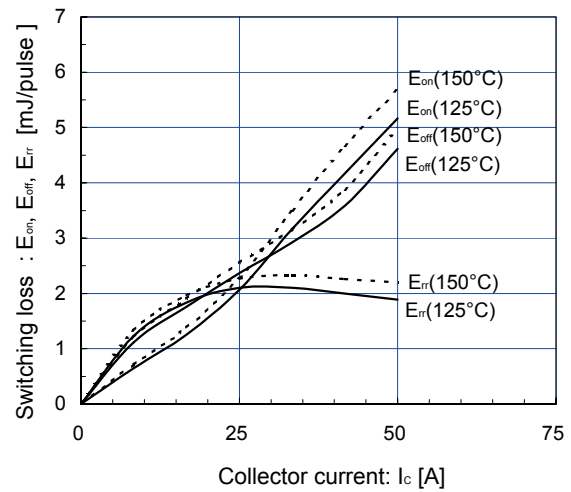
[ Inverter ]

Switching time vs. gate resistance (typ.)  
 $V_{CC}=600V, I_C=25A, V_{GE}=\pm 15V, T_J=125^\circ C$



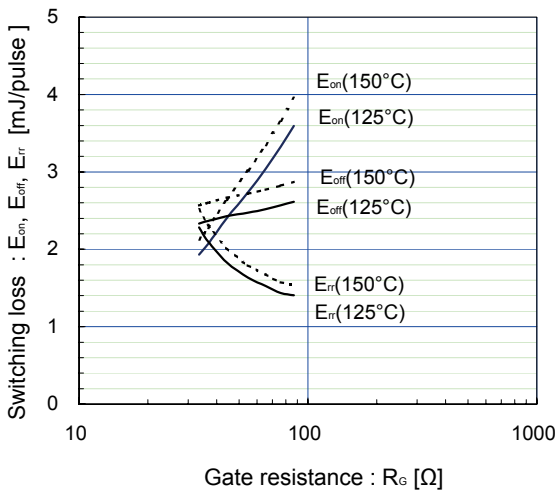
[ Inverter ]

Switching loss vs. Collector current (typ.)  
 $V_{CC}=600V, V_{GE}=\pm 15V, R_G=39\Omega$



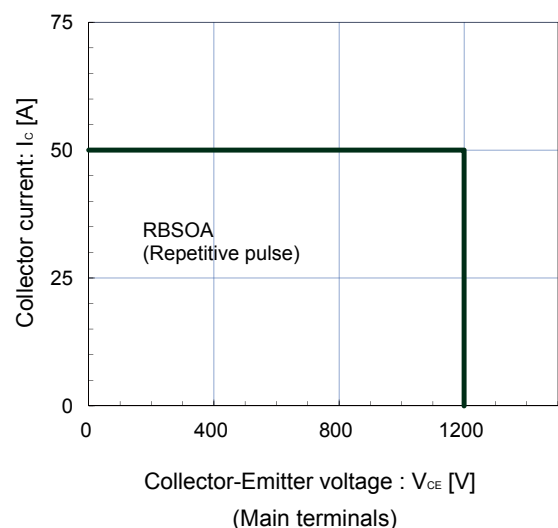
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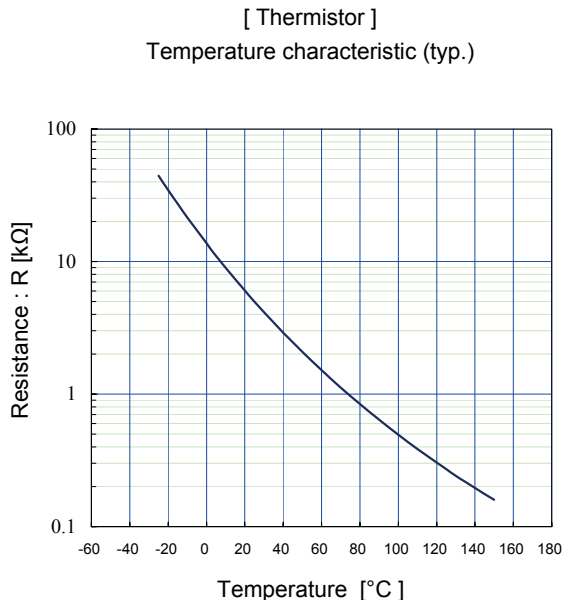
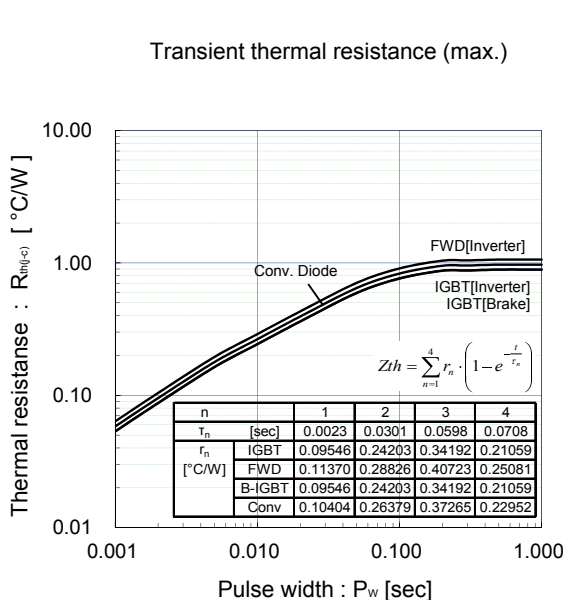
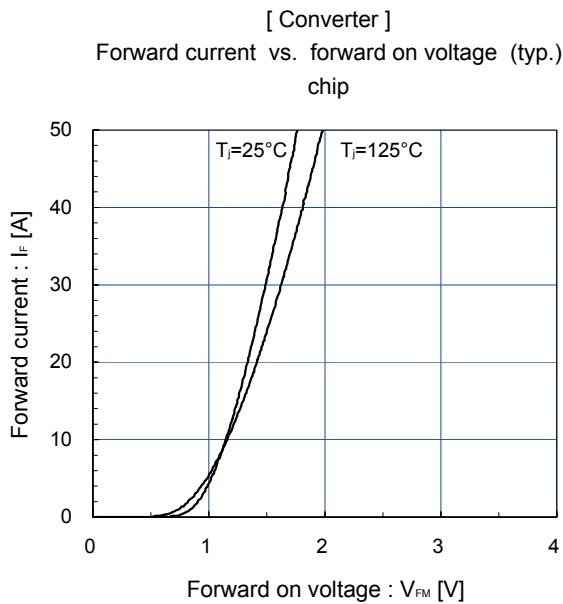
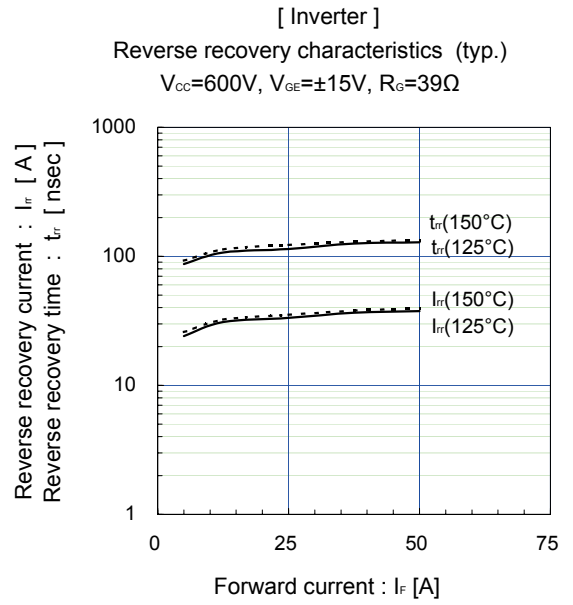
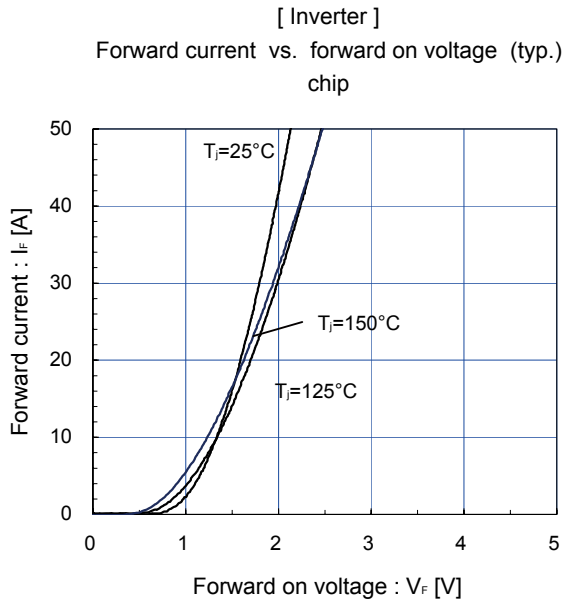
Switching loss vs. gate resistance (typ.)  
 $V_{CC}=600V, I_C=25A, V_{GE}=\pm 15V$



[ Inverter ]

Reverse bias safe operating area (max.)  
 $+V_{GE}=15V, -V_{GE} \le 15V, R_G \ge 39\Omega, T_J=150^\circ C$

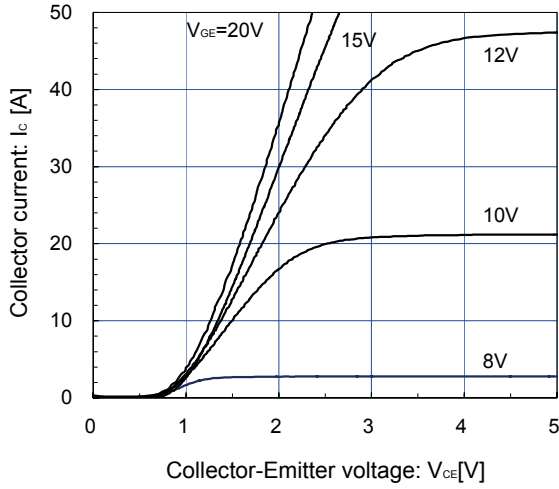




[ Brake ]

Collector current vs. Collector-Emmitter voltage (typ.)

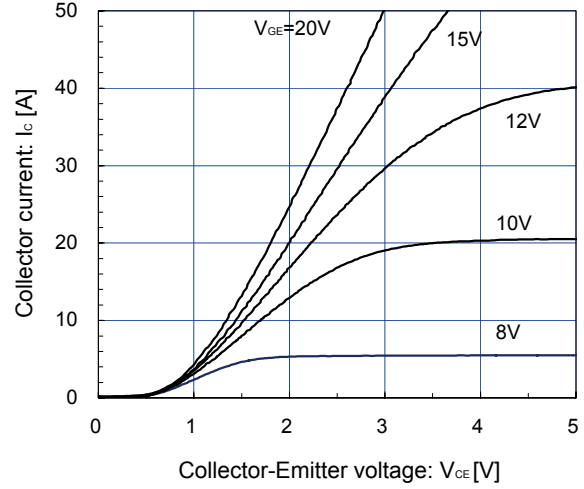
$T_j = 25^\circ\text{C}$  / chip



[ Brake ]

Collector current vs. Collector-Emmitter voltage (typ.)

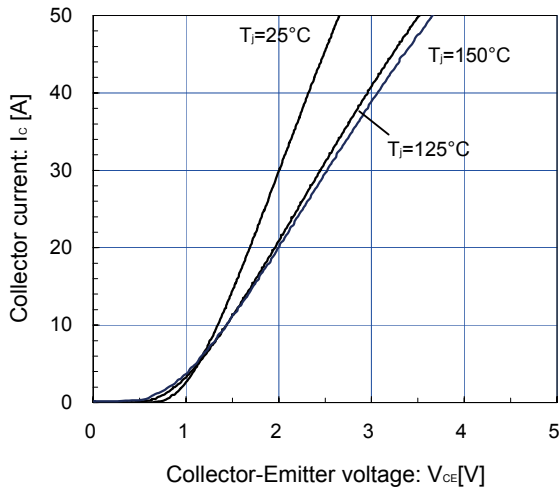
$T_j = 150^\circ\text{C}$  / chip



[ Brake ]

Collector current vs. Collector-Emmitter voltage (typ.)

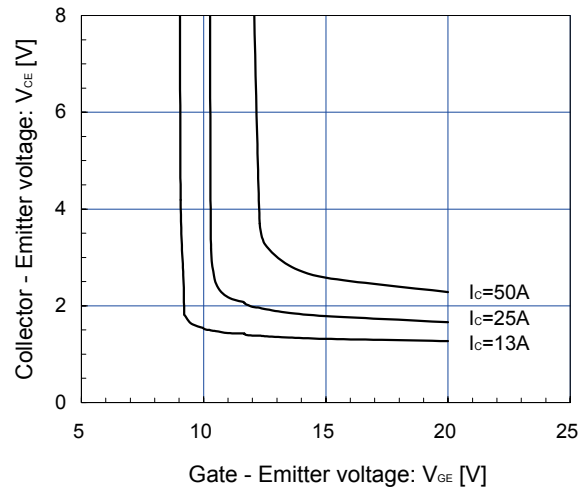
$V_{GE} = 15\text{V}$  / chip



[ Brake ]

Collector-Emmitter voltage vs. Gate-Emmitter voltage (typ.)

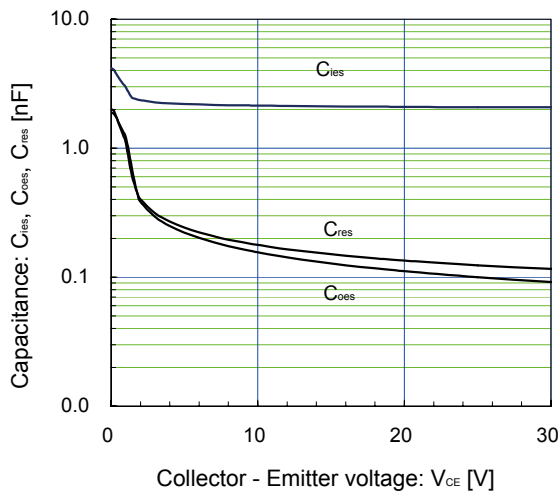
$T_j = 25^\circ\text{C}$  / chip



[ Brake ]

Capacitance vs. Collector-Emmitter voltage (typ.)

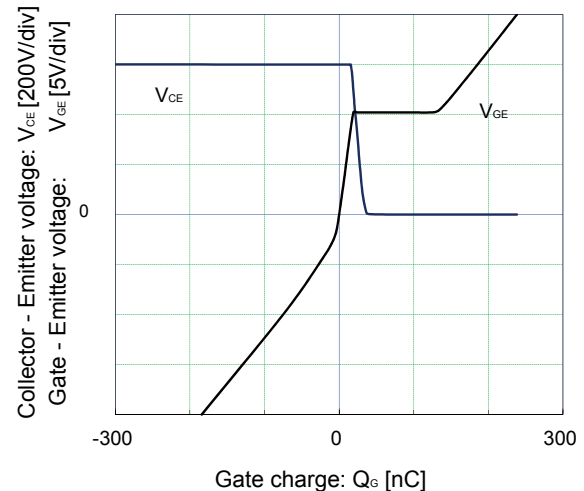
$V_{GE} = 0\text{V}$ ,  $f = 1\text{MHz}$ ,  $T_j = 25^\circ\text{C}$



[ Brake ]

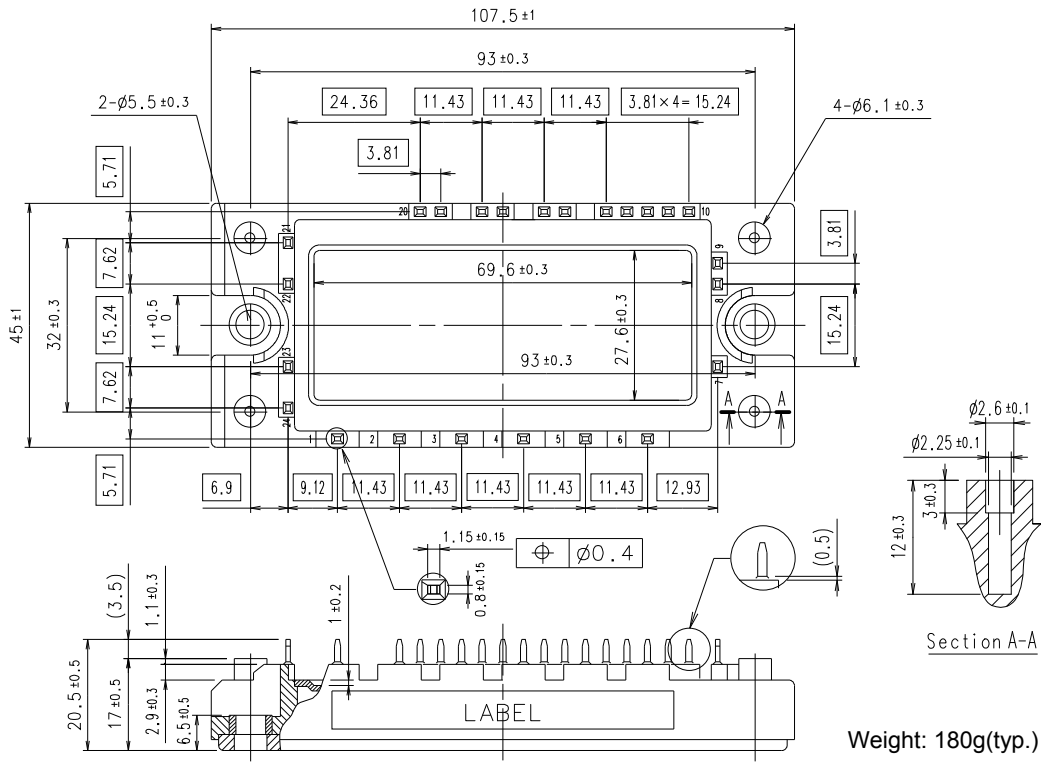
Dynamic gate charge (typ.)

$V_{CC} = 600\text{V}$ ,  $I_c = 25\text{A}$ ,  $T_j = 25^\circ\text{C}$

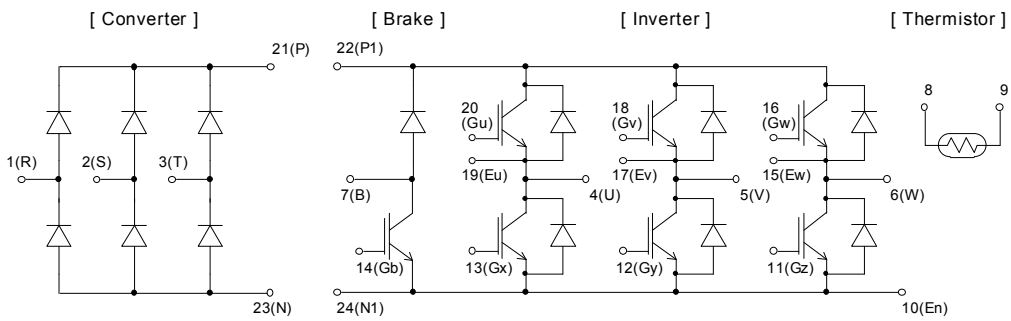


■ Outline Drawings (Unit : mm)

□ shows theoretical dimension.  
 ( ) shows reference dimension.



■ Equivalent Circuit



**WARNING**

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